

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Ronnie M. Harrison                          Attorney Docket No.: 500800.04 (30099/US/3)  
Filed : Concurrently herewith  
Title : METHOD AND CIRCUIT FOR LIMITING A PUMPED VOLTAGE

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior Application No. 10/267,225, filed October 8, 2002, which is a divisional of Application No. 09/996,452, filed November 28, 2001, issued February 10, 2004 as Patent No. 6,690,148. The references listed on the attached Form PTO-1449 were submitted to and/or cited by the Patent and Trademark Office in these prior applications and, therefore, are not required to be provided in this application. If the Examiner wishes, copies will be provided upon request. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

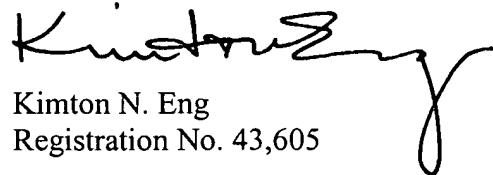
Applicant also wishes to make known to the Patent and Trademark Office that an application, Serial No. 10/267,278, describing and claiming subject matter that is similar to the subject matter of this application, was filed on October 8, 2002. This related application, which is a divisional of Application No. 09/996,452, filed November 28, 2001, issued February 10, 2004 as Patent No. 6,690,148, as well as any prior art cited therein, may be material to the examination of this application. The specification, claims, abstract and drawings of this related application as

filed are identical to those currently on file in the instant application as filed, therefore a duplicate copy is not submitted herewith.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



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KNE:ajs

Enclosures:

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Form PTO-1449

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FORM PTO-1449 (REV.7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 500800.04 (30099/US/3)	APPLICATION NO. Not Yet Assigned
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANT(S) <b>Ronnie M. Harrison</b>			
		FILING DATE Concurrently herewith	GROUP ART UNIT Not Yet Assigned		

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,327,026	07/05/94	Hardee et al.	307	482	
	AB	5,694,035	12/02/97	Keeth	323	349	
	AC	5,698,972	12/16/97	Keeth	323	349	
	AD	5,903,502	05/11/99	Porter	365	201	
	AE	6,018,236	01/25/00	Keeth	323	313	
	AF	6,297,624 B1	10/02/01	Mitsui et al.	323	316	
	AG	6,417,726 B1	07/09/02	Kitade	327	543	
	AH						
	AI						
	AJ						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES      NO
	AK						
	AL						
	AM						
	AN						
	AO						

**OTHER PRIOR ART** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AP	Gregorian, R. et al., "Analog MOS Integrated Circuits for Signal Processing", New York, A Wiley-Interscience Publication, 1986. pp. 146-156.
AQ	

EXAMINER	DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).